Docket Number: 081498-0306614

Slient Reference: GNC26US

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

الملاق 1 2 2004 نق In Se the Application of

RAMPTON E ELLIS

Group Art Unit:

Application No.: 10/684,657

Examiner:

Filed: October 15, 2003

Confirmation No.: 3226

For: GLOBAL NETWORK COMPUTERS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. As this application was filed after June 30, 2003, no copies of the cited U.S. patents are enclosed; however, one copy of each of the other cited references is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the references has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

These references were cited in three Office Actions in three co-related U.S. applications, and in an International Search Report, dated February 6,2004, in a corresponding foreign application. The German language reference has an English Abstract.

Respectfully Submitted,

Carlo M. Cotrone

Registration Number 48715 Customer Number: 00909

Date: August 12, 2004

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Complete if Known Substitute for form 1449A/PTO Application Number 10/684,657 Filing Date INFORMATION DISCLOSURE 10/15/2003 First Named Inventor **ELLIS E FRAMPTON** STATEMENT BY APPLICANT Art Unit **Examiner Name** (Use as many sheets as necessary

Attorney Docket Number Sheet 081498-0306614

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	^{US-} 5,862,357	01-19-1999	HAGERSTEN et al.	
	2	US- 5,774,337	06-30-1998	LEE et al.	
	3	^{US-} 5,570,270	10-29-1996	NAEDEL et al.	
		US-			

	FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Cite No. ¹		atent Document e ³ -Number ⁴⁻ Kind Code ⁵ (<i>if known</i>)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Т6		
	Α	DE	40 08 335	A1	09-26-1991	VIELHABEN				

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6 Applicant is to place a check mark here if English language Translation is attached.

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Substitute for	form 1449B/PTO			Complete if Known		
				Application Number	10/684,657	
	RMATION DIS			Filing Date	10/15/2003	
STAT	EMENT BY A	APPLI	CANT	First Named Inventor	FRAMPTON E ELLIS	
				Art Unit		
	(Use as many sheets as	necessary))	Examiner Name		
Sheet	1	of	1	Attorney Docket Number	081498-0306614	

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
	A	NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, (October 28, 1996).				
	В	LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).				
***************************************	С	DICKINSON ET AL., "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, (October 2-4, 1989).				
	D	NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 (No. 6), p. 1031-1040, (June 6, 1995).				
	Е	ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, (October 23-24, 1995).				
	F	WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, (October 4-9, 1992).				
						

Examiner	Date	
Signature	Considered	

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